Chief Architect

JOB OVERVIEW

Axiado is a San José, California-based company that is reshaping the future of cybersecurity for the world. As such, we embrace diversity and equal opportunity in a serious way.

As the Chief Architect for Axiado, reporting directly to the CTO, you will be a leading technologist for the company and help drive the company's technology roadmap. You will have accountability for managing and contributing to the development of fundamental ASIC technology for all product lines within the company. Your technical responsibilities will include signal-path architecture development, system-level simulation and partitioning, firmware implementation, and specification discovery through all layers of the technology stack.

The candidate will be expected to lead the assessment and balance development cost, schedule, and risk, and will be asked to make insightful and critical decisions about design and architecture options based on clear, thorough trade-off analysis. By helping drive appropriate hardware and software product definition and technology development, you will support our marketing leaders in delivering compelling products to our customers.

The incumbent must have a strong, multi-disciplinary technical background with the ability to interact closely with other leaders and teams to drive results across organizational boundaries, to include marketing, silicon engineering, firmware/algorithms, applications engineering, and sales. Even though this is an individual contributor role at present, we expect to increase the scope as Axiado grows.

KEY RESPONSIBILITIES

- Ownership of all stages of the product development pipeline, including:

    o   early discussions with marketing and lead customers to assess product requirements;

    o   initial architecture development and specification discovery;

    o   tight interaction with silicon and firmware teams to assess implementation tradeoffs;

    o   engage with customers, marketing and IP vendors on product/application requirements and design specifications;

- Define optimal PPA (power/performance/area) SoC architecture, and formulate micro-architecture and design specification for security processing IPs and implementation;

- Conduct system performance analysis and optimization, ASIC bring-up, FW partition and rapid feature development and deployment, along with consultation and support on customer field support issues;

- Provide new FW architecture solutions and IP roadmaps to drive future development while adding underpinning to support current development;

- Serve as a resource for FW development teams for technological and process information, especially as it relates to security, helping resolve architectural and implementation issues as they arise; and

- Evangelize secure firmware development methodologies and tools, assuring work is implemented to the plan via design and code reviews.

QUALIFICATIONS AND SKILLS

- Significant prior direct experience in silicon design, preferably in digital design/CPU/security, and silicon device drivers, and firmware;

- Track record defining compelling and complex SoCs and successfully driving the specification discovery, architecture development, trade-off analysis and deployment of these products to market;

- Strong management skills providing hands-on technical leadership for individual growth while inspiring and motivating multi-disciplinary engineering teams for maximum cross-functional impact;

- Excellent skills in technical communication, strategic vision, and consensus-building; and

- Advanced degree in electrical engineering, computer science, or related field.

To apply for this role, please submit your cover letter and resume to recruitment@axiado.com.